

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1 **Claim 1 (currently amended):** A method to optimize
2 energy consumption in a hearing device in which one of
3 several hearing programs can be selected, the method
4 comprising the steps of

5 [[[-]]] receiving information regarding a selected
6 hearing program, and

7 [[[-]]] adjusting a clock frequency of a clock signal
8 driving processing units of the hearing device,

9 wherein knowledge of computing power needed by the
10 selected hearing program is taken into account for
11 adjusting the clock frequency.

1 **Claim 2 (currently amended):** The method of claim 1,
2 further comprising the step of

3 [[[-]]] adjusting a supply voltage supplying processing
4 units with energy in the hearing device,

5 wherein ~~the~~ knowledge of computing power needed by the
6 selected hearing program is taken into account for
7 adjusting the supply voltage.

1 **Claim 3 (original):** The method of claim 2, further
2 comprising the step of generating the supply voltage from
3 a battery voltage that is higher than the supply voltage.

1 **Claim 4 (original):** The method of claim 2, further
2 comprising the step of generating a memory supply voltage
3 from a battery voltage that is lower than the memory supply
4 voltage.

1 **Claim 5 (currently amended):** The method of claim 2,
2 further comprising the step of either generating the supply
3 voltage or ~~the~~ memory supply voltage from ~~the~~ battery
4 voltage at any point in time.

1 **Claim 6 (currently amended):** The method of claim 2,
2 further comprising the step of using a charge storing
3 device, ~~particularly with~~ at least one capacitor, to
4 generate the supply voltage and/or a memory supply voltage.

1 **Claim 7 (currently amended):** The method of claim 6,
2 further comprising the step of using the ~~same~~ capacitor or
3 capacitors, ~~respectively~~, to generate the supply voltage as
4 well as the memory supply voltage.

1 **Claim 8 (original):** The method of one of the claims 1
2 to 7, further comprising the step of generating essentially

3 a 50%-duty cycle for the clock signal.

1 **Claim 9 (currently amended):** A method to optimize
2 energy consumption in a hearing device in which one of
3 several hearing programs can be selected, the method
4 comprising the steps of

5 [[-]] receiving information regarding a selected
6 hearing program, and

7 [[-]] adjusting a supply voltage supplying processing
8 units with energy in the hearing device,

9 wherein knowledge of computing power needed by the
10 selected hearing program is taken into account for
11 adjusting the supply voltage.

1 **Claim 10 (currently amended):** The method of claim 9,
2 further comprising the step of

3 [[-]] adjusting a clock frequency of a clock signal
4 driving processing units of the hearing device,

5 wherein knowledge of computing power needed by the
6 selected hearing program is taken into account for
7 adjusting the clock frequency.

1 **Claim 11 (original):** The method of claim 9, further
2 comprising the step of generating the supply voltage from
3 a battery voltage that is higher than the supply voltage.

1 **Claim 12 (original):** The method of claim 9, further
2 comprising the step of generating a memory supply voltage
3 from a battery voltage that is lower than the memory supply
4 voltage.

1 **Claim 13 (currently amended):** The method of claim 9,
2 further comprising the step of either generating the supply
3 voltage of ~~the~~ memory supply voltage from ~~the~~ battery
4 voltage at any point in time.

1 **Claim 14 (currently amended):** The method of claim 9,
2 further comprising the step of using a charge storing
3 device, particularly~~with~~ at least one capacitor, to
4 generate the supply voltage and/or a memory supply voltage.

1 **Claim 15 (currently amended):** The method of claim 14,
2 further comprising the step of using the ~~same~~ capacitor or
3 ~~capacitors, respectively,~~ to generate the supply voltage as
4 well as the memory supply voltage.

1 **Claim 16 (original):** The method of one of the claims
2 9 to 15, further comprising the step of generating
3 essentially a 50%-duty cycle for the clock signal.

1 **Claim 17 (currently amended):** A hearing device
2 comprising

3 [[[-]] a processing unit driven by a clock signal,
4 [[[-]] a control unit, and
5 [[[-]] an oscillator unit,
6 whereaswherein the control unit is operatively
7 connected to the oscillator unit which is operatively
8 connected to the processing unit, wherein the clock signal
9 generated by the oscillator unit is adjustable by the
10 control unit via the oscillator unit by taking into account
11 knowledge of computing power needed by a selected hearing
12 program.

1 **Claim 18 (currently amended):** The hearing device of
2 claim 17, further comprising a source unit wherein the
3 control unit is operatively connected to the source unit,
4 and wherein a supply voltage to supply the processing unit
5 with energy is adjustable by the control unit via the
6 source unit by taking into account knowledge of computing
7 power needed by a selected hearing program.

1 **Claim 19 (currently amended):** The hearing device of
2 claim 18, whereinfurther comprising a voltage converter
3 [[is]] provided in the source unit, the voltage converter
4 being able to generate the supply voltage as well as a
5 memory supply voltage whereas, wherein the supply voltage
6 is lower than a battery voltage and the memory supply
7 voltage is higher than the battery voltage.

1 **Claim 20 (original):** The hearing device of claim 19,
2 wherein the voltage converter is able to either generate
3 the supply voltage or the memory supply voltage at any
4 point in time.

1 **Claim 21 (currently amended):** The hearing device of
2 claim 20, ~~wherein~~further comprising a charge storing
3 device, ~~in particular with~~ at least one capacitor, ~~is~~
4 operatively connected to the voltage converter.

1 **Claim 22 (currently amended):** The hearing device of
2 claim 21, wherein ~~the same~~ charge storing device is used to
3 generate the supply voltage as well as the memory supply
4 voltage.

1 **Claim 23 (currently amended):** The hearing device of
2 one of the claims 17 to 22, wherein essentially a 50%-duty
3 cycle for the clock signal is ~~generate-able~~generatable in
4 the oscillator unit.

1 **Claim 24 (currently amended):** A hearing device
2 comprising
3 [[-]]] a processing unit for processing acoustic
4 signals,
5 [[-]]] a control unit, and

6 [[[-]] a source unit generating a supply voltage,
7 whereaswherein the control unit is operatively
8 connected to the source unit which is operatively connected
9 to the processing unit, wherein the supply voltage
10 generated by the source unit is adjustable by the control
11 unit via the source unit by taking into account knowledge
12 of computing power needed by a selected hearing program.

1 **Claim 25 (currently amended):** The hearing device of
2 claim 24, whereinfurther comprising an oscillator unit—is
3 provided, wherein the control unit is operatively connected
4 to the oscillator unit and wherein thea clock signal
5 generated by the oscillator unit is adjustable by the
6 control unit via the oscillator unit by taking into account
7 the knowledge of computing power needed by a selected
8 hearing program.

1 **Claim 26 (currently amended):** The hearing device of
2 claim 24, whereinfurther comprising a voltage converter
3 [[is]] provided in the source unit, the voltage converter
4 being able to generate the supply voltage as well as a
5 memory supply voltage, whereaswherein the supply voltage is
6 lower than a battery voltage and the memory supply voltage
7 is higher than the battery voltage.

1 **Claim 27 (original):** The hearing device of claim 26,

2 wherein the voltage converter is able to either generate
3 the supply voltage or the memory supply voltage at any
4 point in time.

1 **Claim 28 (currently amended):** The hearing device of
2 claim 27, ~~wherein~~further comprising a charge storing
3 device, ~~in particular with~~ at least one capacitor, ~~is~~
4 operatively connected to the voltage converter.

1 **Claim 29 (currently amended):** The hearing device of
2 claim 28, wherein the ~~same~~ charge storing device is used to
3 generate the supply voltage as well as the memory supply
4 voltage.

1 **Claim 30 (currently amended):** The hearing device of
2 one of the claims 24 to 29, wherein essentially a 50%-duty
3 cycle for the clock signal is ~~generate-able~~generatable in
4 the oscillator unit.